WEST

Help | Logout | Interrupt

Main Menu | Search Form | Posting Counts | Show S Numbers | Edit S Numbers | Preferences | Cases

Search Results -

Term	Documents
(12 AND 14).USPT,PGPB,JPAB,EPAB,DWPI,TDBD.	19
(L12 AND L14).USPT,PGPB,JPAB,EPAB,DWPI,TDBD.	19

Database:
- US Patents Full-Text Database
- US Pre-Grant Publication Full-Text Database
- JPO Abstracts Database
- EPO Abstracts Database
- Derwent World Patents Index
- IBM Technical Disclosure Bulletins

Search: Refine Search

Recall Text | Clear

Search History

DATE: Monday, April 28, 2003 Printable Copy Create Case

Set Name side by side	Query	Hit Count	Set Name result set
DB=USPT,PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ			
L15	L12 and l14	19	L15
L14	L13 same cache	350	L14
L13	(status or state or dirty) same (RAM or random access memory) same (table or directory or structure or list)	7345	L13
L12	(snoop or snooping) same cache same dirty	292	L12
L11	(status or state or dirty) same (RAM or random access memory)	48826	L11
L10	L9 and (l4 or l5)	14	L10
L9	cache same (flush or flushing) same (write adj back) same l3	15	L9
L8	L7 and (l5 or l4)	105	L8
L7	cache same (flush or flushing) same (write adj back)	272	L7
L6	cache same (flush or flushing) same (write adj back) same l3 same (l4 or l5)	8	L6
L5	("L3" or (level adj three) or (third adj level)) adj2 cache	496	L5
L4	("L2" or (level adj two) or (second adj level)) adj2 cache	3482	L4
L3	("L1" or (level adj one) or (first adj level)) adj2 cache	2329	L3
L2	operating system same l1	9	L2
L1	(variable or programmable) same cache same (way or set) same (associativity or associative)	114	L1

END OF SEARCH HISTORY

Search Results

Search Results for: [cache and (linked list or list structure)]
Found 764 of 108,036 searched. → Rerun within the Portal

Warning: Maximum result set of 200 exceeded. Consider refining.

Search within Results

GO > Advanced Search > Search Help/Tips

Sort by: Title Publication Publication Date Score

Results 1 - 20 of 200 **short listing**

Prev Page 1 2 3 4 5 6 7 8 9 10 Next Page

1 Cache coherence in systems with parallel communication channels & many processors 98%
John C. Willis , Arthur C. Sanderson , Charles R. Hill
Proceedings of the 1990 ACM/IEEE conference on Supercomputing
November 1990
This paper describes and analyzes two algorithms for maintaining cache coherence in multiprocessor systems with parallel communication channels and many processors. A distributed link-list relates all cache frames representing the same main memory block. Messages traverse the list to maintain list integrity, exclusive ownership, and consistent values. Memory access semantics are equivalent to a shared memory system without caches. Reference latency, efficiency of memory use, and hardware complex ...

2 The software lookaside buffler reduces search overhead with linked lists 96%
Gerald Bozman
Communications of the ACM March 1984
Volume 27 Issue 3